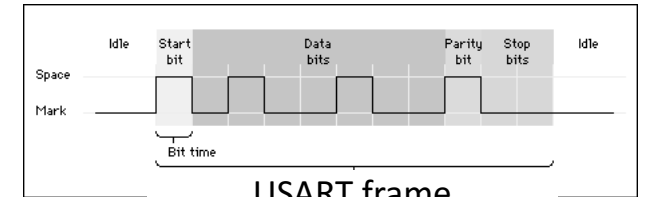


Cheatsheet ATmega324a 2018 rev1

$$f_{CPU} = 16MHz, V_{CC} = 3.3V$$

(PCINT8/XCK0/T0) PB0	1	40	PA0 (ADC0/PCINT0)
(PCINT9/CLKO/T1) PB1	2	39	PA1 (ADC1/PCINT1)
(PCINT10/INT2/AINO) PB2	3	38	PA2 (ADC2/PCINT2)
(PCINT11/OC0A/AIN1) PB3	4	37	PA3 (ADC3/PCINT3)
(PCINT12/OC0B/SS) PB4	5	36	PA4 (ADC4/PCINT4)
(PCINT13/ICP3/MOSI) PB5	6	35	PA5 (ADC5/PCINT5)
(PCINT14/OC3A/MISO) PB6	7	34	PA6 (ADC6/PCINT6)
(PCINT15/OC3B/SCK) PB7	8	33	PA7 (ADC7/PCINT7)
RESET	9	32	AREF
VCC	10	31	GND
GND	11	30	AVCC
XTAL2	12	29	PC7 (TOSC2/PCINT23)
XTAL1	13	28	PC6 (TOSC1/PCINT22)
(PCINT24/RXD0/T3) PD0	14	27	PC5 (TDI/PCINT21)
(PCINT25/TXD0) PD1	15	26	PC4 (TDO/PCINT20)
(PCINT26/RXD1/INT0) PD2	16	25	PC3 (TMS/PCINT19)
(PCINT27/TXD1/INT1) PD3	17	24	PC2 (TCK/PCINT18)
(PCINT28/XCK1/OC1B) PD4	18	23	PC1 (SDA/PCINT17)
(PCINT29/OC1A) PD5	19	22	PC0 (SCL/PCINT16)
(PCINT30/OC2B/ICP) PD6	20	21	PD7 (OC2A/PCINT31)



USART frame

API LCD Text

```
void LCD_init(void); // Initializare LCD
void LCD_writeInstr(uint8_t instr); // Scrie o instructiune catre LCD.
void LCD_writeData(uint8_t data); // Scrie date catre LCD.
void LCD_putChar(char c); // Scrie caracter pe LCD la cursor
void LCD_putCharAt(uint8_t addr, char c); // Scrie caracter la adresa data
void LCD_print(const char* msg); // Afiseaza string la cursor.
void LCD_printAt(uint8_t addr, const char* msg); // Afiseaza string la adresa
```

API LCD Grafic

```
/* Initializeaza Display-ul grafic. */
void ST7735R_Begin();
/* Deseneaza o linie de la (x0, y0) la (x1, y1),
 * avand culoarea data de parametrii r, g, b. */
void ST7735R_Line(int x0, int y0, int x1, int y1,
                 uint8_t r, uint8_t g, uint8_t b);
/* Afiseaza un text, incepand de la pozitia (x, y).
 * Textul are culoarea specificata de pereche (r, g, b).
 * Background-ul pe care este afisat textul are culoarea (bgR, bgG, bgB). */
void ST7735R_DrawText(int x, int y, const char *text,
                     uint8_t r, uint8_t g, uint8_t b,
                     uint8_t bgR, uint8_t bgG, uint8_t bgB);
/* Deseneaza un cerc cu centrul la coordonatele (x, y),
 * de raza 'radius', cu culoarea specificata. */
void ST7735R_Circle(int x, int y, uint8_t radius,
                  uint8_t red, uint8_t green, uint8_t blue);
/* Deseneaza un cerc cu centrul la coordonatele (x, y) de raza 'radius'
 * si il umple cu culoarea data. */
void ST7735R_FilledCircle(int x, int y, uint8_t radius,
                        uint8_t red, uint8_t green, uint8_t blue);
/* Deseneaza un dreptunghi cu coltul stanga sus dat de (x0, y0)
 * si coltul dreapta jos dat de (x1, y1).
 * Dreptunghiul este umplut cu culoarea (r, g, b). */
void ST7735R_FillRect(x0, y0, x1, y1, r, g, b);
```

Extras

```
cli(); // dezactiveaza intreruperi
sei(); // activeaza intreruperi
```

ADMUX	REFS1	REFS0	ADLAR			MUX2	MUX1	MUX0
ADCSRA	ADEN	ADSC	ADATE	ADIF	ADIE	ADPS2	ADPS1	ADPS0
ADCSRB						ADTS2	ADTS1	ADTS0
ADC	Registru pe 16 biti (10 biti aliniati dupa ADLAR, default dreapta)					ADTS	PS	
Câmp	Descriere					000	Free-running	
REFS	Referință tensiune					001		
ADLAR	Rezultat ajustat stânga					010	INT0	
MUX	Indexul canalului					011	Timer0 comp	
ADEN	Enable ADC					100	Timer0 OVF	
ADSC	Start conversie					101	Timer1 COMPB	
ADATE	Enable auto-triggering					110	Timer1 OVF	
ADIF	1 în timpul conversiei					REFS1..0	parity	
ADIE	Înterupere ADC complete					00	AREF	
ADPS	Prescaler pentru conversie					01	AVCC	
ADTS	Selectie eveniment auto-trigger					10	1.11V	
						11	2.56V	

UCSROA	RXC0		UDRE0			U2X0	
UCSR0B	RXCIE0		UDRIE0	RXEN0	TXEN0	UCSZ02	
UCSR0C			UPM01	UPM00	USBS0	UCSZ01	UCSZ00
UDR0	Registru de transmisie/receptie, citirea se face din alt buffer față de scriere					UCSZ02..0	size
UBRR0	Registru pe 16 biti pentru configurarea baud rate					000	5-bit
						001	6-bit
						010	7-bit
						011	8-bit
						UPM01..0	parity
						00	disabled
						01	-
						10	even
						11	odd

$$U2X0==0 \quad BAUD = \frac{f_{cpu}}{16 \cdot (1 + UBRR)}$$

$$U2X0==1 \quad BAUD = \frac{f_{cpu}}{8 \cdot (1 + UBRR)}$$

SPCR	SPIE	SPE	DORD	MSTRL		SPR1	SPR0
SPSR	SPIF						SPI2X
SPDR	Registru buffer					SPR1..0	prescaler
						00	4
						01	16
						10	64
						11	128

PCICR					PCIE3	PCIE2	PCIE1	PCIE0
PCMSK3	PCINT31	PCINT30	PCINT29	PCINT28	PCINT27	PCINT26	PCINT25	PCINT24
PCMSK2	PCINT23	PCINT22	PCINT21	PCINT20	PCINT19	PCINT18	PCINT17	PCINT16
PCMSK1	PCINT15	PCINT14	PCINT13	PCINT12	PCINT11	PCINT10	PCINT09	PCINT08
PCMSK0	PCINT07	PCINT06	PCINT05	PCINT04	PCINT03	PCINT02	PCINT01	PCINT00

Pentru Pin Change Interrupt (întrerupere pe orice schimbare de nivel pe un pin), activăm bitul respectiv din PCMSKx și apoi PCIEx corespunzător PCMSK-ului

DDRX (A B C D)	1 dacă pinul este output, 0 altfel
PORTX (A B C D)	Pt output reprezintă valoarea, pentru input este activarea rezistențelor de pull-up
PCINTX(A B C D)	Valoarea citită de la fiecare pin din portul respectiv

Mod	Frecvență	Duty
normal	$f_{OVF} = \frac{f_{cpu}}{PS \cdot (1 + MAX)}$	$D_{OCxy} = 50\%$
FPWM	$f_{OCR} = \frac{f_{cpu}}{PS \cdot (1 + TOP)}$	$D_{OCxy} = \frac{1 + OCRxy}{1 + TOP}$ (non-inverting)
PWM/PC PWM/PFC	$f_{OCR} = \frac{f_{cpu}}{2 \cdot PS \cdot (1 + TOP)}$	$D_{OCxy} = \frac{1 + OCRxy}{1 + TOP}$ (non-inverting)

TCCR1A	COM1A1	COM1A0	COM1B1	COM1B0			WGM11	WGM10
TCCR1B				WGM13	WGM12	CS12	CS11	CS10
TIMSK1						OCIE1B	OCIE1A	TOIE1

CS12..0	PS		WGM13..0	type	TOP	OVF
000	stop		0000	normal	0xFFFF	MAX
001	1		0001	PWM PC	0x00FF	BOTTOM
010	8		0010	PWM PC	0x01FF	BOTTOM
011	64		0011	PWM PC	0x03FF	BOTTOM
100	256		0100	CTC	OCR1A	MAX
101	1024		0101	FPWM	0x00FF	TOP
			0110	FPWM	0x01FF	TOP
			0111	FPWM	0x03FF	TOP

COM1A1..0	Normal mode	FastPWM	PWM/PC
00	-	-	-
01	Toggle OC1x	Mod 14/15 – toggle OC1A	Mod 9/11 – toggle OC1A
10	Clear on comp	Clear on comp Set on bottom	Clear when ↑ Set when ↓
11	Set on comp	Set on comp Clear on bottom	Clear when ↓ Set when ↑

TCCR0A	COM0A1	COM0A0	COM0B1	COM0B0			WGM01	WGM00
TCCR0B					WGM02	CS02	CS01	CS00
TIMSK0						OCIE0B	OCIE0A	TOIE0

CS02..0 la fel ca CS12..0

COM0A1..0	Normal mode	FastPWM	PWM/PC	WGM02..0	type	TOP	OVF
00	-	-	-	000	normal	0xFF	MAX
01	Toggle OC0x	Mod 7 – toggle OC0A	Mod 5 – toggle OC0A	001	PWM PC	0xFF	BOTTOM
10	Clear on comp	Clear on comp Set on bottom	Clear when ↑ Set when ↓	010	CTC	OCR0A	MAX
11	Set on comp	Set on comp Clear on bottom	Clear when ↓ Set when ↑	011	FPWM	0xFF	MAX
				100	-	-	-
				101	PWM/PC	OCR0A	BOTTOM
				110	-	-	-
				111	FPWM	OCR0A	TOP

TCCR2A	COM2A1	COM2A0	COM2B1	COM2B0			WGM21	WGM20
TCCR2B					WGM22	CS22	CS21	CS20
TIMSK2						OCIE2B	OCIE2A	TOIE2

CS22..0 la fel ca CS12..0

WGM22..0 la fel ca WGM02..0

COM2A1..0 la fel ca COM0A1..0

COM2B1..0 la fel ca COM0B1..0

CS22..0	PS		WGM22..0	type	TOP	OVF
000	stop		000	normal	0xFF	MAX
001	1		001	PWM PC	0xFF	BOTTOM
010	8		010	CTC	OCR0A	MAX
011	32	PWM – pulse width modulation	011	FPWM	0xFF	MAX
100	64	FPWM – Fast PWM	100	-	-	-
101	128	PWM/PC – PWM phase correct	101	PWM/PC	OCR0A	BOTTOM
110	256	PWM/PFC – PWM phase and frequency correct	110	-	-	-
111	1024	TOP – până la cât numără un timer	111	FPWM	OCR0A	TOP
		MAX – maximul până la cât poate numără un timer				

Comune Timere

Timer 1

Timer 0

Timer 2

ADC

USART

SPI

PCINT

GPIO